EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S85	10	"717"/$.ccls. library fpga xilinx valid busy	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/11 08:25
S84	62	"717"/$.ccls. library fpga xilinx	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/11 08:25
S83	48	716/?.ccls. filter fpga xilinx library	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/11 08:22
S82	62	716/?.ccls. filter fpga xilinx	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/11 08:22
S81	223	716/?.ccls. filter fpga	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/11 08:21
S80	50	(filter fpga xilinx) (filter same fpga) valid busy	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/09 12:10
S79	403	(filter fpga xilinx) (filter same fpga)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/09 12:10
S70	55	(dtf or fft or rfd) fpga library xilinx	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/09 11:36
S65	51	(dtf or fft or rfd) fpga (gate$1way $1 or (gate adj way)) dsp	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/09 10:33

S64	1	(dtf or fft or rfd) fpga (gate$1way $1 or (gate adj way)) (v$1out$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/09 10:33
S63	85	(dtf or fft or rfd) fpga (gate$1way $1 or (gate adj way))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/09 10:33
S62	1	rfd fpga (gate $1way$1 or (gate adj way)) (fft or dft)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/09 10:32
S61	6	((signal or fpga or out$1put$3) near4 valid) fpga dsp (co$1simulat $4 or simulat$4). ab.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/08 16:22
S60	21	((signal or fpga or out$1put$3) near4 valid) dsp (co$1simulat$4 or simulat$4).ab.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/08 16:21
S59	11	(radix-4 or (radix adj "4")) fpga (real (accept$4 or ready) (valid (sample or frame)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 11:57
S58	12	(radix-4 or (radix adj "4")) fpga (real (valid (sample or frame)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 11:57
S57	7	(radix-4 or (radix adj "4")) fpga ((fpga or out$1put $4) same (valid (sample or frame)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 11:51
S56	6	(fpga same dsp same (co $1simulat$4 or (co adj simulat $4)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 11:48

S55	98	(fpga same dsp same (co $1simulat$4 or simulat$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 11:48
S50	58	(scope or oscillo $1scope$1) (co $1simulat$4 or simulat$4).ab. fpga ((fpga or out $1put$4) same valid)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 11:33
S49	713	(scope or oscillo $1scope$1) (co $1simulat$4 or simulat$4).ab. fpga	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 11:32
S48	713	(scope or oscillo $1scope) (co $1simulat$4 or simulat$4).ab. fpga	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 11:32
S47	25	(co$1simulat$4) fpga dsp (out $1put$4 same valid)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 11:02
S43	28	fpga (out$1put$4 same ((done or valid) (v$1out or vout)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:51
S42	1	fpga (out$1put$4 same ((done or valid) (v$1out or vout))) (fft or dft)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:51
S41	28	fpga (out$1put$4 same ((done or valid) (v$1out or vout)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:51
S40	1	fpga (out$1put$4 same (done valid (v$1out or vout)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:51

S39	16	fpga (out$1put$4 same (done (v $1out or vout)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:50
S38	4	cooley$9 (radix-4 or radix4 or (radix adj "4")) (fft or dft) fpga	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:41
S37	69	cooley$9 (radix-4 or radix4 or (radix adj "4")) (fft or dft)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:41
S36	70	cooley$9 (radix-4 or radix4 or (radix adj "4"))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:41
S35	1	colley$9 (radix-4 or radix4 or (radix adj "4"))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:41
S32	48	(fft or dft) (radix-4 or "radix4" or (radix adj "4")) fpga	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:32
S31	525	(fft or dft) (radix-4 or "radix4" or (radix adj "4"))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:31
S30	5	(fft or dft) (valid real imaginary (vout or v$1out))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:29
S29	5	(fft or dft) (valid real imaginary vout)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:29
S28	1381	(fft or dft) (valid real imaginary)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:29

S24	121	(radix-4 or radix4) (fpga or cooley$9)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:21
S23	3	(radix-4 or radix4) fpga cooley$9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:19
S22	70	(radix-4 or radix4) fpga	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:18
S21	2	(radix-4) (v$1out or vout) fpga	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:18
S20	2	(radix-4) (v$1out or vout)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:15
S19	1	(radix-4 cooley$1tukey) (v$1out or vout)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:15
S18	1	(radix-4 same cooley$1tukey) (v$1out or vout)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:15
S17	6	(radix-4 same cooley$1tukey) (valid or done or (v$1out or vout))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:13
S16	1	(radix-4 same cooley$1tukey) valid done (v$1out or vout)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:13
S15	8	(radix-4 same cooley$1tukey) fft	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:11

S14	36	(radix-4 cooley $1tukey) fft	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:11
S13	2	(radix-4 near3 cooley$1tukey) fft	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:11
S12	0	(radix$4 near3 cooley$1tukey) fft co$1simulat$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:10
S11	42	(radix$4 near3 cooley$1tukey) fft	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:10
S10	42	(radix$4 near3 cooley$1tukey)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:10
S9	98	radix$4 cooley $1tukey	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:10
S8	501	fft (co$1simulat $4 or simulat$4) dsp fpga (scope or oscillo $1scope)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/07 10:06
S7	22	(co$1simulat$4 or simulat$4) dsp fpga (scope or oscillo$1scope) ((real imagi$8) near4 (input or data or signal)) (valid or in $1valid) fft	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/06 17:18

S6	70	(co$1simulat$4 or simulat$4) dsp fpga (scope or oscillo$1scope) ((real imagi$8) near4 (input or data or signal)) (valid or in$1valid)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/06 17:18
S5	76	(co$1simulat$4 or simulat$4) (dsp or cpu) fpga (scope or oscillo$1scope) ((real imagi$8) near4 (input or data or signal)) (valid or in$1valid)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/06 17:17
S4	421	(co$1simulat$4 or simulat$4) (dsp or cpu) fpga (scope or oscillo$1scope) real imagi$8 (valid or in$1valid)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/06 17:17
S3	27	co$1simulat$4 dsp fpga (scope or oscillo$1scope) real imagi$8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/06 17:14
S2	27	co$1simulat$4 dsp fpga scope real imagi$8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/06 17:10
S1	113	co$1simulat$4 dsp fpga scope	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/12/06 17:10

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